



Sheet 1 of 1

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| Form PTO-1449<br>(REV. 8-83)  |  | US Dept. of Commerce<br>PATENT & TRADEMARK OFFICE  |      | ATTY DOCKET NO.<br>104822        |       | APPLICATION NO.<br>09/424,667 |  |
| INFORMATION DISCLOSURE STATEMENT<br><br>(Use several sheets if necessary)   |  |  |      | APPLICANTS<br>Makoto KUDO et al. |       |                               |  |
|   |  |  |      | FILING DATE<br>March 15, 2000    |       | GROUP<br>2114                 |  |
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| OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)  |  |  |      |                                  |       |                               |  |
| BFB   |  | Isao Yoshida, "Function and Application of Serial Port", Transistor Gijutsu, Vol. 32, No. 10, October 1, 1995, pp. 204-247 |      |                                  |       |                               |  |
|   |  | (w/partial English language translation of article)  |      |                                  |       |                               |  |
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| EXAMINER<br><br>Bryce P. Bongo  |  |  |      | DATE CONSIDERED<br>5/4/05        |       |                               |  |
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